

	Name	Mrs. Meena Chavan		
	Designation	Assistant Professor		
	Department	Electronics Engineering		
	Qualification	M.Tech (Electronics-VLSI), Ph.D(Scholar)		
	Contact No.	020-24107390	Ph. Extension	272
	Email ID	mschavan@bvucoep.edu.in		
	Experience	Teaching :	13.8 Years	Industry :

Area Of Interest	VLSI, Speech processing			
Publications	International Journal (s):	22	National Journals (s) :	nil
	International Conference (s):	02	National Conference:	06
Publication Details	<p>Write publication details as per example given below- author name, paper name, journal/conference name, year</p> <p>International Journal:</p> <ol style="list-style-type: none"> 1. Meena Chavan, "SOBEL edge detection using HDL codes", IJET, Mar-April 2018, vol 4, issue 4. 2. Ms. Meena Patil, "Separation of Hindi source signal in BSS using GDA-AGSO approach", IJMTER, March 2018 Vol 5, issue 3. 3. Ms. Meena Patil, "Modified GSO Based Kurtosis Maximization Criterion for BSS in Hindi Speech Processing System Help of LDA", International Journal of Engineering and Techniques, Jan - Feb 2018, Volume 4 Issue 1 4. Prof. Meena Chavan, "Hardware Co-simulation of Sobel edge detection using FPGA and System generator", IJIRCCE, Nov. 2017, Vol 5, issue 11. 5. Prof. Meena Chavan " An efficient data hiding approach on digital color image for secret communication, IJETT, Oct. 2017 Vol. 52, Issue 3. 6. Mrs Meena Chavan, "VLSI based automated digital person counter and smart appliance control system for energy conservation", IJIRSET, Aug 2017, Vol 6, issue-8. 7. Prof. Meena Chavan "A review paper on digital image steganography techniques", IJRASET, May 2017 issue V, vol 5. 8. Mrs. Meena Chavan, "Alcohol Detection and vehicle control system", IRJET, May 2017, Vol-4, issue-5 9. Mrs Meena Chavan, "VLSI based automated digital person counter and smart appliance control system for energy conservation", IJIRCCE, May 2017 Vol 5, 			

issu-5.

10. Mrs. M.S.Chavan, "Implementation of 16-point Radix-4 FFT algorithm", IJAREEIE, April 2017 vol6, issu4.
11. Prof. Meena Chavan, "Hardware Co-simulation of Sobel edge detection using FPGA and System generator", IJRASET, May 2017, Vol5, issue V
12. Mrs. Meena Chavan, "Detection Of Moving Object Using Background Subtraction CT-DWT Domain," IJECCE, May 2016 Vol7 Issu3.
13. Mrs. Meena Chavan, "Compressing Encrypted Images &Data Using Auxiliary Information", IJECCE, May 2016, Vol7 Issu3.
14. Mrs. Meena Chavan, "Over speed alarm system for two wheelers", IJCR, May2016, Vol 8, Issu 5.
15. Mrs. Meena Chavan, "Smart real time pollution monitoring &controlling System using FPGA Spartan 3 and Android", IJSETR, March 2016 Issu 6 Vol 5.
16. Prof. Meena Chavan, "Smart real time pollution monitoring System using FPGA and Android", IJSART, Dec.2015.Vol1,issu12.
17. Meena Chavan, " A wireless approach towards medical surveillance system", IJARCCCE, Dec 2015 , vol4,issu12.
18. Mrs. M. S. Chavan ," FPGA based multifunctional wireless medical surveillance system," IJARCCCE-May2015, ISSN No. 2319-5940
19. Mrs. M. S. Chavan ,"Source separation and Echo cancellation using Independent component analysis and DWT" IJAREEIE- May2015, Vol4,issu5 ISSN No. 2320-3765
20. Mrs. M.S. Chavan," Blind source separation & echo cancellation using discrete wavelet transform and ICA," IJSRM-2015, Vol3, issu6 ISSN No. 2321-3418.
21. Mrs. M. S. Chavan ," Development of vibration measurement and tilt control ," IJEEDC-2014,ISSN No.- 2320-2084
22. Mrs. M. S. Chavan ," Finger Print Detection for Medical Application," IJARCCSEE- 2013 ,ISSN No.- 2277-9043

International Conference:

1. Mrs. M. S. Chavan ," VLSI based speech synthesis", ICEVD2008
2. Mrs. M. S. Chavan," GSM based remote monitoring system", ICEVD2008

National Conference:

1. Mrs. M. S. Chavan ," Eye Ball Tracking Wheel Chair ," NACICE -2013,
2. Mrs. M. S. Chavan ," MIMO Technology to advance Wireless Transmission

	<p>Networks" NACICE -2013,</p> <ol style="list-style-type: none"> 3. Mrs. M. S. Chavan , " Wireless Automotive Communications", NACICE - 2013 4. Mrs. M .S. Chavan, " Automatic Pantry System Using Zigbee Moudle", NACICE -2013 5. Mrs. M.S. Chavan,"Embedded based safe Hybrid system for cars", NCECIS-2011 6. Mrs. M.S. Chavan,"Solaries 10 os zonar technology for visulization concept" NCECIS-2011 <p>National level Symposium:</p> <ol style="list-style-type: none"> 1. Mrs. M.S. Chavan, "4G Technology".
Books Published	nil
Professional Memberships	ISTE, IFERP
WorkShop/ Seminar/Conference attended	<ol style="list-style-type: none"> 1. Attended National Teacher's Congress-2 at MIT WPU, Pune, from 10-12th Jan2018 2.Attented Workshop , 'Springer Author', at BVUCOE, Pune Mar2017 3. Successfully completed Short term training program on "Recent trends in advanced communication and Embedded Systems from BV's COE, Kolhapur from 24-29th Dec.2016. 4. Attended one week Faculty Development Program On" Advanced Embedded Technology" At SIT COE, Yadrav, Ichalkaranji, Dec.2016 5. Attended Workshop on "photonic Design and simulation tools" at BVUCOE, Pune, Nov. 2015. 6. Attended one week Workshop on "IC nanometer design using Mentor Graphics tool" at BVUCOE,Pune 31Aug-Sep 2015 7. Workshop" Secrets of Guru", BVDUCOE,Pune, Aug 2015. 8. Workshop "Women Empowerment," at BVDUCOE,Pune,Feb.2015. 9.Workshop "Embedded system design using ARM cores", PICT, Pune Dec. 2014 10.Workshop " Fundamentals of Cloud Computing", BVDUCOE, Pune Aug 2013 11.Workshop "LABVIEW" ,BVDUCOE,Pune Jan.2013 12. Certificate course in "CMOS VLSI Design", PICT, Pune,Sep. 2012 13. Seminar "MATLAB &Simulink", Mathworks,Pune 2011 14. Workshop "ROBOTICS",MIT,Pune 2011 15. STTP "Advanced VLSI &Microelectronics Design", MIT,Pune Mar2010 16. Workshop "Hands on PIC Microcontroller" SKN, Pune, Feb 2010 17. Workshop "3G Technology", PVPIT, Pune 2009 18.Attended and completed five days short term training program(STTP) on "Advanced VLSI" from Bharati Vidyapeeth University College of Engineering, Pune.2008 19. Completed a three days workshop on FACE (Faculty Awareness Camp on Entrepreneurship) from Bharati Vidyapeeth University College of Engineering, Pune. 2008.

	20. Workshop "Teaching Engineering Effectively", BVDUCOE,Pune2006
Achievements	02 patents filed
Extra Activities	<ol style="list-style-type: none"> 1. Admission committee member. 2. Bharatiyam committee member. 3. Workshop coordinator (ESDAP-15,ESD-16,ESD&IoT-17,PCB design & Fabrication). 4. Unit Test Coordinator. 5. Participated in Swachh Bharat Abhiyan Organized By Department of Electronics BVUCOE at Vitthalwadi,Pune. 6. Participated in Swachh Bharat Abhiyan Organized By BVUCOE at Pune Station. 7. Participated in 'Beti Bachao, Beti Padhao' Abhiyan at Khedshivapur. 8. Board of Studies Member. 9. NBA coordinator.