

**BHARATI VIDYAPEETH**  
(DEEMED TO BE UNIVERSITY)  
**COLLEGE OF ENGINEERING**  
**DHANKAWADI, PUNE – 411043**  
**Electrical Engineering Department**  
**Class Time Table (TERM -I A.Y. 2018-19)**

**CLASS: B. Tech. (Electrical)**

**SEM: III**

**CLASS ROOM NO: WW-II-209**

(w.e.f. 28 /08 / 2018)

**GFM: - - Prof. Mr. V. V. Mehtre**

(Mobile -9764821359)

TIME	MON	TUE	WED	THU	FRI	TIME	SAT
9.15	Network Security  <b>Sem-III NS-Lab</b>	EMI-B S S Hirve WW II 217 DCT-C V V Mehtre WW II 202 LDIC-A M Mukkawar WW II 205		DCT-A V V Mehtre WW II 202 EM/C-I-B A R Soman WW II 206 LDIC-C M Mukkawar WW II 205	EMI-C S S Hirve WW II 217 EM/C-I-A A R Soman WW II 206 LDIC-B M Mukkawar WW II 205	8.30	EM/C-I Dr. D S Bankar
10.15						9.30	EM-III Tutorial Visiting
11.15						10:30	STAFF MEETING
11:30	EM/C-I Dr. D S Bankar	LDIC M Mukkawar	EM/C-I Dr. D S Bankar	LDIC M Mukkawar	PSD-III	11:30	EMI S S Hirve
12:30	EMI S S Hirve	PSD-III	EM-III Visiting	EM-III Visiting	EM/C-I Dr. D S Bankar	12:30	LDIC M Mukkawar
1:30	<b>R E C E S S</b>						
2:15	DCT V V Mehtre	EM-III Visiting	DCT V V Mehtre	EMI S S Hirve	EMI-A S S Hirve WW II 216 DCT-B V V Mehtre WW II 202 EM/C-I-C A R Soman WW II 206		
3:15	PSD-III	DCT V V Mehtre	PSD-III				

Batches: A-1 to 21; B- 22 to 42; C- 43 Onwards

Faculty Name	Subject	L	T	P	Total
Mrs.Madhuri Mukkawar	Linear & Digital Integrated Circuits (LDIC)	3		6	09
Mr. V. V. Mehtre	Digital Computational Techniques (DCT)	3		6	09
Dr. D. S. Bankar	Electrical Machines – I (EM/C-I)	4		6	10
Prof. Mr. S. S. Hirve	Electrical Measurements & Instrumentation (EMI)	3		6	09
Visiting Faculty Member	Engineering Mathematics–III (EM-III)	3	1		04
Visiting Faculty Member	Professional skill development- 3 (PSD-III)	4			04
Total Load		20	1	24	45