

BHARATI VIDYAPEETH
(DEEMED TO BE UNIVERSITY)
COLLEGE OF ENGINEERING
DHANKAWADI, PUNE – 411043
Electrical Engineering Department
Class Time Table (TERM -I A.Y. 2018-19)

CLASS: B. Tech. (Electrical)

SEM: III

CLASS ROOM NO: WW-II-209

(w.e.f. 04 /07 / 2018)

GFM: - - Prof. Mr. V. V. Mehtre

(Mobile -9764821359)

TIME	MON	TUE	WED	THU	FRI	TIME	SAT
9.15	Network Security Sem-III NS-Lab	EMI-B S S Hirve WW II 217 DCT-C V V Mehtre WW II 202 EM/C-I-D A R Soman WW II 206 LDIC-A M Mukkawar WW II 205		EMI-D S S Hirve WW II 217 DCT-A V V Mehtre WW II 202 EM/C-I-B A R Soman WW II 206 LDIC-C M Mukkawar WW II 205	EMI-C S S Hirve WW II 217 DCT-D V V Mehtre WW II 202 EM/C-I-A A R Soman WW II 206 LDIC-B M Mukkawar WW II 205	8.30	EMI S S Hirve
10.15						9.30	EM-III Tutorial Dr. Shilpa Patil
11.15						10:30	STAFF MEETING
11:30	EM/C-I A R Soman	LDIC M Mukkawar	EM/C-I A R Soman	LDIC M Mukkawar	PSD-III G P Bonsale	11:30	EM/C-I A R Soman
12:30	EMI S S Hirve	PSD-III G P Bonsale	EM-III Dr. Shilpa Patil	EM-III Dr. Shilpa Patil	EMI S S Hirve	12:30	LDIC M Mukkawar
1:30	R E C E S S						
2:15	DCT V V Mehtre	EM-III Dr. Shilpa Patil	DCT V V Mehtre	EM/C-I A R Soman	EMI-A S S Hirve WW II 216 DCT-B V V Mehtre WW II 202 EM/C-I-C A R Soman WW II 206 LDIC-D M Mukkawar WW II 205		
3:15	PSD-III G P Bonsale	DCT V V Mehtre	PSD-III G P Bonsale				

Batches: A-1 to 19; B- 20 to 38; C- 39 to 57; D- 58 onwards

Faculty Name	Subject	L	T	P	Total
Mrs.Madhuri Mukkawar	Linear & Digital Integrated Circuits (LDIC)	3		8	11
Mr. V. V. Mehtre	Digital Computational Techniques (DCT)	3		8	11
Prof. Mrs. A. R. Soman	Electrical Machines – I (EM/C-I)	4		8	12
Prof. Mr. S. S. Hirve	Electrical Measurements & Instrumentation (EMI)	3		8	11
Prof. Dr. Shilpa Patil	Engineering Mathematics–III (EM-III)	3	1		04
Visiting	Professional skill development- 3 (PSD-III)	4			04
Total Load		20	1	32	53