

BHARATI VIDYAPEETH (DEEMED TO BE UNIVERSITY)
COLLEGE OF ENGINEERING
DHANKAWADI, PUNE – 411043
Electronics Engineering Department
Class Time Table(TERM -I A.Y. 2018-19)

CLASS: M. Tech. (Electronics-VLSI) SEM: I CLASS ROOM NO: W-416

PG Coordinator: VikasKaduskar (Mobile -8983368947)

(w.e.f. 16/07/2018)

TIME	MON	TUE	WED	THU	FRI	TIME	SAT
9:15	ESP MVP W 304	DVLSI ARP W 305		ESP MSC	ACS NTM	8.30	ACS NTM
10:15				ESP MSC	ACS NTM	9.30	ACS NTM
11.15	R E C E S S					10.30	Staff meeting
11.30	DVLSI RBG		ESP AAS	LA & RP VF	LA & RP VF	11:30	
12.30	DVLSI RBG		ESP AAS	LA & RP VF	LA & RP VF	12:30	
1.30	R E C E S S						
2.15			DVLSI RBG				
3.15			DVLSI RBG				

Faculty Name	Subject	L	T	P	Total
A.A.SHINDE	Embedded System& Processors	02	-	-	02
M.S.CHAVAN	Embedded System& Processors	02			02
M.V. PATIL	Embedded System& Processors	-	-	02	02
R.B. GHONGADE	Digital VLSI	04	-	00	04
A.R.PATIL	Digital VLSI	-	-	02	02
N.T.MARKAD	ACS- Advanced Communication System;	04	-	-	04
VISITING FACULTY	LA & RP- Linear Algebra & Random processes;	04	-	-	04
Total		16	00	04	20