

BHARATI VIDYAPEETH (DEEMED TO BE UNIVERSITY)
COLLEGE OF ENGINEERING
DHANKAWADI, PUNE – 411043
Electronics Engineering Department
Class Time Table(TERM –IA.Y. 2018-19)

CLASS: M. Tech. (Electronics-VLSI) SEM: III

CLASS ROOM NO: PG Lab (PSOC W-304)

PG Coordinator: VikasKaduskar (Mobile -8983368947)

(w.e.f. 04/07/2018)

TIME	MON	TUE	WED	THU	FRI	TIME	SAT
9:15		ELECTIVE-II (AFVDA VF W-416	ELECTIVE-I (TVVD) SKO W-416			8.30	Self Study-I
10:15		ELECTIV II (AFVDA)VF W-416	ELECTIVE I (TVVD) SKO W-416			9.30	
11.15	R E C E S S						
11.30	ELECTIVE I TVVD SPT W-304	ELECTIVE II (AFVDA) VF W-305			ELECTIVE-II (AFVDA) VF W-304	11:30	Dissertation - I
12.30					ELECTIV II (AFVDA) VF W-304	12:30	
1.30	R E C E S S						
2.15	ELECTIVE-I (TVVD) SKO W-416			Self Study-I	Dissertation- I		
3.15	ELECTIV I (TVVD) SKO W-416						

Faculty Name	Subject	L	T	P	Total
S.K. OZA	TESTING & VERIFICATION OF VLSI DESIGN	04	-	-	04
S. P. TONDARE	TESTING & VERIFICATION OF VLSI DESIGN			02	02
VISITING FACULTY	ALOGORITHMS FOR VLSI DESIGN IN AUTOMATION	04	-	02	06
	DISSERATION REPORT-I	-	-	04	04
	SELF STUDY -I				