

BHARATI VIDYAPEETH (DEEMED TO BE UNIVERSITY)
COLLEGE OF ENGINEERING
DHANKAWADI, PUNE – 411043
ElectronicsEngineering Department
Class Time Table(TERM -I A.Y. 2018-19)

CLASS: **B.Tech. (Electronics)** SEM: **III** CLASS ROOM NO: W-301

(w.e.f. 04/07/2018)

GFM :-Mrs S. K. PAWAR

TIME	MON	TUE	WED	THU	FRI	TIME	SAT
9:15	PSD-III	CT SPT	C-AE-ARP-W-403; D-CT-SPT-W307B; E-DLC-SVD-W310; F-SS-MVP-W302	EM-III SMP	E-AE-JSM-W-403; F-CT-ASN-W307B; A-DLC-SRP-W310; B-SS-SVD-W302	8.30	F-AE-JSM-W-403; A-CT-SPT-W307B; B-DLC-PRN-W310; C-SS-MVP-W302
10:15	PSD-III	DLC PRN		AE JSM		9.30	
11.15	R E C E S S					10:30	STAFF MEETING
11:30	A-AE-ARP-W-403; B-CT-ASN-W307B;	B-AE-SPG-W-403; C-CT-SPT-W307B;	AE JSM	D-AE-SPG-W-403; E-CT-SPT-W307B; F-DLC-SRP-W310; A-SS-MVP-W302	EM-III SMP	11:30	PSD-III
12:30	C-DLC-PRN-W310; D-SS-VPK-W302	D-DLC-SVD-W310; E-SS-MVP-W302	DLC PRN		AE JSM	12.30	PSD-III
1:30	R E C E S S						
2:15	CT SPT	AE JSM	SS MVP	EM-III (Tut) SMP	SS MVP		
3:15	SS MVP	EM-III SMP	CT SPT		DLC PRN		

Faculty Name	Subject	L	T	P	Total
J.S.MORBALE	AE - ANALOG ELECTRONICS	04	-	04	08
S. P. TONDARE	CT- CIRCUIT THEORY	03	-	08	11
P.R. NAREGALKAR	DLC -DIGITAL LOGIC CIRCUITS	03	-	04	07
M. V. PATIL	SS- SIGNALS & SYSTEMS	03	-	08	11
S.M. PATIL	EM-III ENGG. MATHS-III	03	01	-	04
A.R. PATIL	AE- ANALOG ELECTRONICS	-	-	04	04
S.P. GAIKWAD	AE- ANALOG ELECTRONICS			04	04
A.S. NIGADE	CT- CIRCUIT THEORY	-	-	04	04
S.V.DHOLE	SS- SIGNALS & SYSTEMS	-	-	04	04
V. P. KADUSKAR	SS- SIGNALS & SYSTEMS	-	-	02	02
S. K. PAWAR	DLC- DIGITAL LOGIC CIRCUITS			04	04
S.V.DHOLE	DLC- DIGITAL LOGIC CIRCUITS			04	04
TOTAL		16	01	50	67