

BHARATI VIDYAPEETH (DEEMED TO BE UNIVERSITY)
COLLEGE OF ENGINEERING
DHANKAWADI, PUNE – 411043
Electronics Engineering Department
Class Time Table (TERM -I A.Y. 2018-19)

CLASS: B.Tech. (Electronics) SEM: VII CLASS ROOM NO: W-301/ 404B
 GFM :-Mr.A.R. PATIL

(w.e.f. 09/08/2018)

TIME	MON	TUE	WED	THU	FRI	TIME	SAT
9:15	PROJECT STAGE-I	ACP-ASN-W-404 B; DIP-SRM-C205; MBC-SVD-W-404B	ACP-ASN-W-404 B;	B-ACP(T)-ASN-W304 MBC-SVD D-CN-SRM W305;	ESD ARP	8.30	PROJECT STAGE-I
10:15		CN SPG	ESD ARP		PLCA PAC	9.30	
11:15	R E C E S S						
11:30	ACS NTM W 301	PLCA AAS W301	C-PLCA-PAC- W303; DIP-SRM- W404B;	ESD ARP W301	CN SPG W404B	10:30	STAFF MEETING
1:30	PLCA PAC W301	ESD ARP W301		ACS NTM W301	ACS NTM W404B	11:30	ACP-ASN- W-301; DIP-SRM- W404B; MBC-SVD- C205
12:30	R E C E S S					12.30	CN SPG
2:15	A-PLCA-PAC- W303; B-CN-SPG- W305;	B-PLCA- HSH-W303; C-CN-SRM- W305;	PLCA AAS	CN SPG W404B	D-PLCA-HSH- W303; A-CN-SPG- W305; D-DIP-SRM(T)- W302		
3:15	C-MBC(T)- SVD-	A-ACP(T)- ASN-W304	ACS NTM W404B	ACP-ASN- C205; DIP- SRM-W404B; MBC-SVD-W- 301			

Faculty Name	Subject	L	T	P	Total
S.P. GAIKWAD	CN-COMPUTER NETWORKS	04	-	04	08
A.A. SHINDE	PLCA-PROGRAMMABLE LOGIC CONTROLLERS & APPLICATIONS	02	-	-	02
P.A. CHOUGULE	PLCA-PROGRAMMABLE LOGIC CONTROLLERS & APPLICATIONS	02	-	04	06
H.S. HEMANE	PROGRAMMABLE LOGIC CONTROLLERS & APPLICATIONS	-	-	04	04
A.R. PATIL	ESD-ELECTRONIC SYSTEM DESIGN	04	-	-	04
N.T. MARKAD	ACS-ADVANCE COMMUNICATION SYSTEM	04	-	-	04
S.R. MADKAR	ELECTIVE-I (DIGITAL IMAGE PROCESSING)	04	02	-	06
A.S. NIGADE	ELECTIVE-I (ADVANCED COMPUTER PROGRAMMING)	04	04	-	08
S. V. DHOLE	ELECTIVE-I (MOBILE & BROADBAND COMMUNICATION)	04	02	-	06
S.R. MADKAR	CN-COMPUTER NETWORKS	-	-	04	04
PROJECT STAGE-I	RESPECTIVE GUIDES	-	-	10	10
TOTAL		28	08	26	60