

BHARATI VIDYAPEETH (DEEMED TO BE UNIVERSITY)
COLLEGE OF ENGINEERING, PUNE
DEPARTMENT OF E&TC

Class: E&TC I SEM IV

(w.e.f. 04/01/2019)

Time	Monday	Tuesday	Wednesday	Thursday	Friday	Time	Saturday
09:15-10:15	LIC -RBG- E&TC I Sem IV C302	LIC -RBG- E&TC I Sem IV C302	LIC -AAS- E&TC I Sem IV C302	CSE-SPD E&TC I SEM IV C302	DSF-DKR E&TC I SEM IV C302	08:30	CSE-PVD E&TC I SEM IV C302
10:15-11:15	AEC-ABP E&TC I SEM IV C302	DSF-DKR E&TC I SEM IV C302	CSE-SPD E&TC I SEM IV C302	AEC-PDK E&TC I SEMIV C302	ACS-AAS E&TC I SEMIV C302	09:30	NS
11:15-11:30	RECESS						
11:30-01:30	LIC-HST-E1 W406B	LIC-HST-E2 W406B	LIC-HST-E3 W406B	DSF-DKR-E1 W407A	DSF-DKR- E2 W407A	10:15	DSF-DKR- E2 W407A
	AEC-ABP-E2 W511	AEC-ABP-E3 W511	AEC-ABP-E1 W511			11:15	
01:30-02:15	RECESS					12:15	
2:15-3:15	DSF-DKR E&TC I SEM IV C302	AEC-ABP E&TC I SEM IV C302	AEC-ABP E&TC I SEM IV C302	ACS-AAS E&TC I SEM IV C302	PSD-E&TC I SEM IV C302	12.30	
3:15-4:15	CSE-SPD E&TC I SEM IV C302	PSD-E&TC I SEM IV C302	ACS-AAS E&TC I SEM IV C302	PSD-E&TC I SEM IV C302			

Sr.No.	Acronym	Subject Name	Lect.	Pract.	Tut.
1	LIC	Linear Integrated Circuits	3	2	0
2	AEC	Applied Electronic Circuits	4	2	0
3	CSE	Control System Engineering	3	0	1
4	DSF	Data Structure and Files	3	2	0
5	ACS	Analog Communication Systems	3	2	0
6	PSD-IV	Professional Skill Development IV	4		
		Total	20	8	1