

BHARATI VIDYAPEETH (DEEMED TO BE UNIVERSITY)
COLLEGE OF ENGINEERING, PUNE
DEPARTMENT OF E&TC

Class: E&TC II SEM IV

(w.e.f. 04/01/2019)

Time	Monday	Tuesday	Wednesday	Thursday	Friday	Time	Saturday
09:15-10:15	DSF-DKR-E4 W407A	LIC-CSM-E4 W406B	LIC-CSM-E5 W406B	LIC-CSM-E6 W406B	CSE-NMS E&TC II SEM IV C402	08:30	DSF-DKR -E6 W407A
10:15-11:15		AEC-ABP-E5 W511	AEC-PDK-E6 W511	AEC-ABP-E4 W511	DSF-PSK E&TC II SEM IV C402	09:30	
11:15-11:30	RECESS						
11:30-12:30	ACS-SCM E&TC II SEM IV C402	DSF-PSK E&TC II SEM IV C402	CSE-NMS E&TC II SEM IV C402	CSE-NMS E&TC II SEM IV C402	AEC-ABP E&TC II SEM IV C402	10:30	NS
12:30-01:30	DSF-PSK E&TC II SEM IV C402	CSE-NMS E&TC II SEM IV C402	AEC-PDK E&TC II SEM IV C402	ACS-SCM E&TC II SEM IV C402	ACS-SCM E&TC II SEM IV C402	11:30	
01:30-02:15	RECESS					12:30	
2:15-3:15	LIC -AAS- E&TC II Sem IV C402	LIC -RBG- E&TC II Sem IV C402	LIC -RBG- E&TC II Sem IV C402	AEC-ABP E&TC II SEM IV C402	DSF-DKR-E5 W407A		PSD-E&TC II SEM IV W 401
3:15-4:15	AEC-ABP E&TC II SEM IV C402	PSD-E&TC II SEM IV W401	PSD-E&TC II SEM IVW401	PSD-E&TC II SEM IV W401			

Sr.No.	Acronym	Subject Name	Lect.	Pract.	Tut.
1	LIC	Linear Integrated Circuits	3	2	0
2	AEC	Applied Electronic Circuits	4	2	0
3	CSE	Control System Engineering	3	0	1
4	DSF	Data Structure and Files	3	2	0
5	ACS	Analog Communication Systems	3	2	0
6	PSD-IV	Professional Skill Development IV	4		
		Total	20	8	1