

BHARATI VIDYAPEETH (DEEMED TO BE UNIVERSITY)**COLLEGE OF ENGINEERING, PUNE – 411043****Electronics Engineering Department****Class Time Table(TERM -II A.Y. 2018-19)**

CLASS: M. Tech. (Electronics-VLSI) SEM: II CLASS ROOM NO: W-416

PG Coordinator: Vikas Kaduskar (Mobile -8983368947)

(w.e.f. 02/01/2019)

TIME	MON	TUE	WED	THU	FRI	TIME	SAT
9:15	ADSP W303-SG	ADSP MVP		WN VPK	AVLSID SKO	8.30	
10:15		AVLSID SKO		WN VPK	WN VPK	9.30	ADSP AAS
11.15	R E C E S S					10:30	STAFE MEETING
11:30	I & V P SG	WN VPK	AVLSID SKO		I & V P SG	11:30	I & V P SG
12:30	AVLSID SKO		I & V P SG		ADSP AAS	12:30	ADSP MVP
1.30	R E C E S S						
2:15			AVLSID-- W305-SG				
3:15							

Faculty Name	Subject	L	T	P	Total
A.A. SHINDE	Advanced DSP	02	-	-	02
M.V. PATIL	Advanced DSP	02	-	-	02
S.K.OZA	Analog VLSI Design	04	-	-	04
S. GHUGARDARE	Image & Video Processing	04	-	-	04
V.P.KADUSKAR	Wireless Network	04	-	-	04
S. GHUGARDARE	Advanced DSP	-	-	02	02
S. GHUGARDARE	Analog VLSI Design	-	-	02	02
Total		16	00	04	20