

**BHARATI VIDYAPEETH
(DEEMED TO BE UNIVERSITY)
COLLEGE OF ENGINEERING, PUNE – 411043
Electronics Engineering Department
Electronics Engineering Department**

Class Time Table(TERM -II A.Y. 2018-19)

CLASS: M. Tech. (Electronics-VLSI) SEM: IV

CLASS ROOM NO: PG Lab (PSOC W-304)

PG Coordinator: Vikas Kaduskar (Mobile -8983368947)

(w.e.f. . 02/01/2019)

TIME	MON	TUE	WED	THU	FRI	TIME	SAT
9:15						8.30	
10:15						9.30	
11:15						10.30	STAFE MEETING
11:30						11:30	Dissertatio n- II
12:30						12:30	
1.30	R E C E S S						
2:15				Self Study-II	Dissertation- II		
3:15							