


| | | | | | | | | | |
|---|---|--|----|-------------------------|----|-------------------------------|----|----------------------|----|
| | | | | | | | | | |
| | | | | | | | | | |
|  | Name | Ms. Payal Sanjay Kadam | | | | | | | |
| | Designation | Assistant professor | | | | | | | |
| | Department | Electronics and Telecommunication | | | | | | | |
| | Qualification | M.Tech. (Electronics & Tele Communication) | | | | | | | |
| | Contact No | 9423524715 Ph. Extension | | | | | | | |
| | E-mail Address | Payalskadam94@gmail.com | | | | | | | |
| | Experience | Teaching : 0.5 Years Industry : | | | | | | | |
| Area Of Interest | VLSI and Embedded System | | | | | | | | |
| Publications | <table border="0"> <tr> <td>International Journal (s):</td> <td>01</td> <td>National Journals (s) :</td> <td>00</td> </tr> <tr> <td>International Conference (s):</td> <td>00</td> <td>National Conference:</td> <td>00</td> </tr> </table> | International Journal (s): | 01 | National Journals (s) : | 00 | International Conference (s): | 00 | National Conference: | 00 |
| International Journal (s): | 01 | National Journals (s) : | 00 | | | | | | |
| International Conference (s): | 00 | National Conference: | 00 | | | | | | |
| Publication Details | Ms. Payal S. Kadam, Prof. S.S.Belsare,"FPGA Implementation of Reconfigurable Processor for Image Processing" International Journal Of Scientific & Engineering Research, Volume 8, Issue 4, April-2017; | | | | | | | | |
| Books Published | | | | | | | | | |
| Professional Memberships | | | | | | | | | |
| WorkShop/Seminar/Conference attended | TEQIP -2 Workshop on ESD using ARM & PSOC Training Programme in HEM ELECTRONICS based on PLC, SCADA Automation Based on PLC, SCADA ,HMI in MPTA Education. | | | | | | | | |
| Achievements | | | | | | | | | |
| Extra Activities | Guardian Faculty Member, Lab In charge of LIC | | | | | | | | |