

	<b>Name</b>	<b>Sudhir Bussa</b>			
	<b>Designation</b>	<b>Assistant Professor</b>			
	<b>Department</b>	<b>Electronics and Telecommunication</b>			
	<b>Qualification</b>	<b>M. Tech (Electronics-Computer)</b>			
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<b>Experience</b>	<b>Teaching :</b>	07 Years	<b>Industry :</b>	nil	

<b>Area Of Interest</b>	Image processing, VLSI, Embedded Systems			
<b>Publications</b>	<b>International Journal (s):</b>	02	<b>National Journals (s) :</b>	00
	<b>International Conference (s):</b>	00	<b>National Conference:</b>	01
<b>Publication Details</b>	<ol style="list-style-type: none"> <li>1. Sudhir Bussa, Chirag Shah, Pinak Patel, Avyay Patel “Implementation of Image enhancement on FPGA”, <i>National conference in Electronics &amp; Instrumentation-2013</i></li> <li>2. “Design of Binary Multiplier using Adders”, <i>International Journal of Electrical and Electronics Research</i> ISSN 2348-6988 (online) Vol. 4, Issue 1, pp: (169-173), Month: January - March 2016</li> <li>3. Nitish Kumar, Kunar Potdar, Sudhir Bussa “Design of Rectangular Micro-Strip Patch antenna for 916 Mhz.”, <i>International Journal of Engineering Sciences &amp; Research Technology</i> ISSN 2277-9655- May 2017</li> </ol>			
<b>Books Published</b>	Nil			
<b>Professional Memberships</b>	Nil			
<b>WorkShop/ Seminar/Conference attended</b>	<ol style="list-style-type: none"> <li>1. International conference in E&amp;TC 2015</li> <li>2. National Conference in E&amp;TC 2014</li> <li>3. Red Hat Linux</li> <li>4. National Conference on Electronics &amp; Instrumentation-2013</li> <li>5. Advanced cloud computing</li> <li>6. PSOC and ARM</li> </ol>			
<b>Achievements</b>	Qualified in Gate-2008,2011,2012,2013			
<b>Extra Activities</b>	Guest speaker at School of Audiology, Bharati Vidyapeeth , External faculty to VIT			