

	Name	Shruti K. Oza		
	Designation	Professor and Head		
	Department	E & Tc		
	Qualification	M.Tech. Ph.D		
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	Email ID	skoza@bvucoep.edu.in		
	Experience	Teaching :	17 years	Industry :

Area of Interest	Analog and Mixed signal VLSI, VLSI Design and Testing, Electronic Circuit Design, Chip design and Fabrication.			
Publications	International Journal (s):	23	National Journals (s) :	--
	International Conference (s):	18	National Conference:	22
Publication Details	Attached in Appendix A			
Books Published	<p>(1) Shruti Oza, N.M. Devashrayee, "Design of CMOS Low Power Folding and Interpolating ADC-Optimization of Area and Power for Medium Resolution Applications", <i>Lambert Academic Publisher [ISBN- 978-3-8484-8018-0], Germany, April-2012.</i></p> <p>(2) Shruti Oza, Amisha Naik, N.M. Devashrayee, "VLSI Technology & Design", <i>Mahajan Publishing House [ISBN- 978-93-81256-07-7], Ahmedabad, February-2013.</i></p> <p>(3) Shruti Oza, Amisha Naik, N.M. Devashrayee, "Introduction to VLSI Design and Technology", <i>Lambert Academic Publisher [ISBN- 978-3-659-58273-8], Germany, August-2014.</i></p> <p>(4) Shruti Oza, , "Low Voltage, Low Power SRAM Design", <i>Lambert Academic Publisher [ISBN- 978-3-659-37680-1], Germany, December-2016.</i></p>			
Professional Memberships	<p>1. Life membership of ISTE (LM 52849)</p> <p>2. Life membership of ISTE (LM 52849) and IMS (315 LM)</p>			
Workshop/Seminar/Conference attended	AttqAppendix B			
Achievements	Best Paper Award, Best Professor Award			
Extra Activities	Organization of Conferences/workshops, Expert talk and session chair as per Appendix C			

Appendix A

Paper Presented/Published:

International Journals (23):

1. Shruti Oza, N.M. Devashrayee, "Low Voltage Folding Amplifier using 130nm Technology", *International Journal of Emerging Technologies and Applications in Engineering Technology & Sciences* [(IJ-ETA-ETS) ISSN: 0974-3588], pp.73-82, July-Dec-2008.
2. Shruti Oza, N.M. Devashrayee, "Design of Low Power, High Speed Folding Amplifier for Folding & Interpolating ADC", *International Journal of Computer Science and Communication Technologies* [(Technia)- ISSN:0974-3375], Vol.1, No.2, pp.185-191, Jan. 2009.
3. Shruti Oza, N.M. Devashrayee, "Low Power Comparator design using 0.13um Technology", *International Journal of Computers, Information Technology and Engineering (IJCITAE)* , [ISSN:0973-743X], Vol.3, No.1, pp.17-21, Jan-June 2009.
4. Shruti Oza, N.M. Devashrayee, "Low Voltage Bootstrap Switch using 0.18- μ m Technology", *International Journal of Advanced Engineering & Applications* [ISSN-0975-7783], Vol.3, No.1, pp.20-24, Jan-2010.
5. Shruti Oza, N.M. Devashrayee, " Low Power Coarse Converter for Folding & Interpolating ADC using 0.18 μ m Technology", *2nd International Joint Journal Conference in Engineering, Track-Electrical and Electronics-IJJCE 2010, IJRTE Vol.3, No.3, pp.201-204, 2010.*
6. Shruti Oza, N.M. Devashrayee, "Folding & Interpolating ADC using Low Power Folding Amplifier", *International Journal of Electronics & Communication Technology*, [ISSN-2230-9543], Vol. 1, Issue 1, pp.49-54, Dec-2010.
7. Jigar Chaudhari, Rachna Jani and Shruti Oza, "A 0.08-7 GHz Low Power Low Noise Amplifier Design Using 0.18 μ m CMOS Technology", *International Journal of Computer Science & Emerging Technologies* [ISSN: 2044 - 6004], Vol.2, No.4, Aug. 2011.
8. Jigisha Sureja, Shruti Oza, "Comparative study of various LNA topologies Used for CMOS LNA Design", *International Journal of Computer Science & Emerging Technologies* [ISSN: 2044 - 6004], Vol.3, No.41, Feb. 2012.

9. Sanket Jadhav, Shruti Oza, "Flash Memory Controller", *International journal of Electrical, Electronics and Data Communication*, [ISSN-2320-2084], Vol. 2, Issue-3, March 2014.
10. Pravin Shette, Shruti Oza, "Study of High Performance AMBA AHB", *International journal of Electrical, Electronics and Data Communication*, [ISSN-2320-2084], Vol. 2, Issue-3, March 2014.
11. Shruti Oza, "FinFET based SRAM Design for Low Power Applications", *International journal of Electrical, Electronics and Data Communication*, [ISSN-2320-2084], Vol. 2, Issue-3, March 2014.
12. Pravin Shette, Shruti Oza, "Design of an Efficient FSM for an Implementation of AMBA AHB Master", *International journal of Advanced Research in Computer Science and Software Engineering*, [ISSN-2277-128X], Vol. 4, Issue-3, March 2014.
13. Sanket Jadhav, Shruti Oza, "Flash Memory Controller", *International journal of Advanced Research in Computer Science and Software Engineering*, [ISSN-2277-128X], Vol. 4, Issue-4, March 2014.
14. Pravin Shette, Shruti Oza, "Design of an AMBA AHB Reconfigurable Arbiter for On-chip Bus Architecture", *International journal of Application or Innovation in Engineering and Management* [ISSN 2319 – 4847], Vol. 3, Issue-5, May 2014.
15. Murtuza Jeeranwala, Shruti Oza, "Implementation of an Efficient 64 point FFT/IFFT Block for OFDM Tranceiver of IEEE 802.11a", *International journal of Application or Innovation in Engineering and Management* [ISSN 2319 – 4847], Vol. 3, Issue-5, May 2014.
16. Sanket Jadhav, Shruti Oza, "Design of Flash Memory Controller", *International journal of Computer Science and Information Technology*, [ISSN-0975-9646], Vol. 5, Issue-3, June 2014.
17. **Amruta Ingle, Shruti Oza," Novel High Speed Vedic Multiplier Using Compressors And Pipelining"** *International journal of Electrical, Electronics and Data Communication*, [ISSN 2321-2950], Vol. 1, Issue-1, April 2015
18. **Vaibhav Shikare, Shruti Oza," Reducing Power Consumption In DRAM Using Partial Access Method "** *International journal of Electrical, Electronics and Data Communication*, [ISSN 2321-2950], Vol. 1, Issue-1, April 2015
19. **Amruta Ingle, Shruti Oza," FPGA Implementation of Novel High Speed Vedic Multiplier"** *International journal of Advanced Research Electrical, Electronics and Instrumentation Engineering*, [ISSN 2320-3765], Vol. 4, Issue-6, June 2015
20. **Vaibhav Shikare, Shruti Oza," Reducing Power Consumption In DRAM Using Partial Access Method "** *International journal of Scientific Research and Education*, [ISSN 2321-7545], Vol. 3, Issue-6, June 2015

21. Rachna Jani, Shruti Oza, "***Design and Simulation of Power Efficient Linear MOS Transconductor,***" *Indian Journal of Science and Technology*, [ISSN-0974-6846], Vol. 9, Issue-40, October-2016
22. Vrinda Kulkarni, Shruti Oza, , "***Design and Implementation of 3D FFT,***" *Indian Journal of Scientific & Engineering Research*, [ISSN-2229-5518], Vol. 8, Issue-4, April-2017
23. Mrs. Mugdha A. Kango, Shruti Oza Rahurkar, Swapnil Thorat, "Implementation of UWB Micro-Strip Antenna with Circular Patch And Wings For Wearable Applications," *IAETSD Journal For Advanced Research In Applied Sciences*, Volume 4, Issue 1, Jan-June /2017

International Level (18):

1. Shruti Oza, N.M. Devashrayee, "Design of Folding Amplifier for Folding & Interpolating ADC using 0.13 μ m Technology", *International Conference on Advanced Computing & Communication Technologies, ICACCT-2008*, Panipat, Haryana, Nov. 2008.
2. Shruti Oza, N.M. Devashrayee, "Low Voltage SRAM Design Using 0.18-Micron Technology" , *International Conference on Emerging Technologies and Applications in Engineering, Technology and Sciences , ICETAETS-2008* , Saurashtra University, Rajkot, Gujarat, Jan. 2008.
3. Shruti Oza, N.M. Devashrayee, "Design of Low Voltage, Low Power Current Sense Amplifier for SRAM", *International Conference on Embedded System, Mobile Communication and Computing, ICEM^{C2}-2007,*, Bangalore, Aug. 2007.
4. Shruti Oza, N.M. Devashrayee, - "Low Voltage, Low Power Current Mode Read-Write SRAM Design using 180 nm technology", *International Conference on Modeling and Simulation, CITICOMS-2007*, Coimbatore , Aug. 2007.
5. Shruti Oza, N.M. Devashrayee, "Design of Folding Amplifier for Folding & Interpolating ADC using 0.13 μ m Technology", *International Conference on Advanced Computing & Communication Technologies, ICACCT-2008*, Panipat, Haryana, Nov. 2008.
6. Shruti Oza,, N.M. Devashrayee, " Low Power Comparator Design using 0.13 μ m Technology, *International Conference on VLSI & Communication , IcVCom-2009*, April 2009.
7. Shruti Oza,, N.M. Devashrayee, "Low Voltage, Low Power Folding Amplifier for Folding & Interpolating ADC", *International Conference on Advances in Recent Technologies in*

*Communication and Computing, ARTCom 2009, ISSN 978-0-7695-3845-7, **Archived in the IEEE Xplore and IEEE CS Digital Library**, Kottayam, Kerala, Octo. 2009.*

8. Shruti Oza,, N.M. Devashrayee, “Low Voltage, High Folding Rate Folding Amplifier”, *International conference on Emerging Trends in Technology-ICWET-2010, **Archived in the ACM Digital library <http://portal.acm.org>***, Thakur College of Engineering and Technology, Mumbai, Maharashtra, Feb. 2010.
9. Shruti Oza,, N.M. Devashrayee, “Low Power Folding & Interpolating ADC using 0.18 μ m Technology”, *International Conference on Advances in Computer Engineering – ACE-2010, **Archived in the IEEE Xplore and IEEE CS Digital Library***, Bangalore, India, June 21-22, 2010.
10. Shruti Oza, N.M. Devashrayee, “Design of 6-bit Folding & Interpolating ADC using Low Power Folding Amplifier”, **14th IEEE/VSI VLSI Design And Test Symposium, VDAT-2010**, Chitkara University, Chandigarh, India, July 2010.
11. Shruti Oza,, N.M. Devashrayee, “Effect of Parameter variations on Low Power Folding & Interpolating ADC”, *1st International Conference on ‘Current Trends in Technology-NUiCONE-2010*, Nirma University, Ahmedabad, India, Dec. 2010.
12. Shruti Oza,, N.M. Devashrayee, “Process Corner Analysis for Folding and Interpolating ADC”, *International Conference on ‘Advances in Information Technology and Mobile Communication-AIM 2011* , **Archived in the Springer Digital Library**, Nagpur, India, April 2011.
13. Shruti Oza,, N.M. Devashrayee, “Low Power Folding and Interpolating ADC using 0.35- μ m Technonolgy”, *2nd International Conference on ‘Current Trends in Technology-NUiCONE-2011*, , **Archived in the IEEE Xplore and IEEE CS Digital Library**, Nirma University, Ahmedabad, India, Dec. 2011.
14. Jigisha Sureja, Shruti Oza, “Design of 0.1-8 GHz Modified Cascode CS Low Power LNA using 0,18 μ m CMOS Technology”, *International Conference on Information, Knowledge & Research in Engineering, Technology and Science-2012, ICIKR-ETS-2012*, G.K.Bharad, Rajkot, March 24-25, 2012.
15. Urvi Darji, Shruti Oza, Kruti Thakor “Low power and Low Jitter Phase Frequency Detector For DPLL using 0.18 μ m CMOS Technology”, *International Conference on Information, Knowledge & Research in Engineering, Technology and Science-2012, ICIKR-ETS-2012*, G.K.Bharad, Rajkot, March 24-25, 2012.

16. Ronak Mistry, Shruti Oza, "Survey of Optimization of NCO for Digital Communication Systemes.", *International Conference on Research & Development in Engineering, Technology and Science-2013, ICRD-ETS-2013*, G.K.Bharad, Rajkot, April 7, 2013..
17. Nirav Chauhan, Shruti Oza, "Survey of Optimization of FFT Processor for OFDM Receiver", *International Conference on Research & Development in Engineering, Technology and Science-2013, ICRD-ETS-2013*, G.K.Bharad, Rajkot, April 7, 2013.
18. Mrs. Mugdha A. Kango, Shruti Oza Rahurkar, "Effect of Dielectric Materials on UWB Antenna for wearable applications" *Intranational conference on Power, Control, Signals and Instrumentation Engineering, ICPCSI-2017*, Chennai, Sept 21-22, 2017

National Level (22):

1. Shruti Oza, N.M. Devashrayee, "Low Voltage nMOS bootstrap switch for sample & hold circuit", *National Conference NUCONE – 2006 at I.T.*, NIT, Ahmedabad, Gujarat, Dec. 2006.
2. Shruti Oza, N.M. Devashrayee, -" Low Voltage, Low Power Current Mode Read SRAM design" *National Level Technical Symposium, Mindbend-2007*, Surat, Gujarat, Feb. 2007.
3. Shruti Oza, N.M. Devashrayee, "Design of Low Power, High Speed Sense Amplifier for SRAM", *National Seminar on Trends in Wireless Technologies, NSTWT-2007*, Wadhawan City, Gujarat, March 2007.
4. Shruti Oza, N.M. Devashrayee, "Low Power, High Speed SRAM Design using 180 nm technology", *National Conference on VLSI & Communication, NC-Vcom-2007*, Kottayam, Kerala, March 2007.
5. Shruti Oza, N.M. Devashrayee, -"Low Voltage SRAM Design using Voltage mode Read-Write", *Innovative Developments in Engineering Applications, IDEA-2007*, Sangrur, Punjab, March 2007.
6. Shruti Oza, N.M. Devashrayee, "Low Voltage, Low Power Current Sense Amplifier for SRAM", *National Conference on Advance Data Computing, Communications & Security, ENVISION-2007*, Kadi, Gujarat, July 2007.

7. Shruti Oza, N.M. Devashrayee, "Design of High Speed, Low Power Sense Amplifier for SRAM", *National Conference IMS - 2007*, Chandigarh, Aug. 2007
8. Shruti Oza, N.M. Devashrayee, "Low Voltage Sense Amplifier Design using 180nm Technology", *National Conference NUCONE-2007*, Ahmedabad, Gujarat, Dec. 2007.
9. Shruti Oza, N.M. Devashrayee, "Impact of Scaling On Leakage Current In Deep Submicron Region", *National Conference on Nanotechnology-NAQC-2008*, Kolkata, West Bengal, Sept. 2008.
10. Shruti Oza, N.M. Devashrayee, "Leakage Current in Deep Submicron Region", *National Conference on Advances in Electronic System Design-2008*, AITS, Rajkot, Gujarat, Nov. 2008.
11. Shruti Oza, N.M. Devashrayee, " Low Voltage, Low Power SRAM design", *National Level Technical Symposium, Projections-2007*, Limbda, Gujarat, Feb. 2007.
12. Shruti Oza, N.M. Devashrayee, " Impact of Scaling On Leakage Current In Deep Submicron Region", *National Conference on Nanotechnology-NAQC-2008*, Kolkata, West Bengal, Sept-2008.
13. Shruti Oza, N.M. Devashrayee, "Leakage Current in Deep Submicron Region", *National Conference on Advances in Electronic System Design-2008* at AITS, Rajkot, Gujarat, Nov. 2008.
14. Shruti Oza, N.M. Devashrayee, "Low Power Folding Amplifier using 130nm Technology", *National Conference NUCONE – 2008* at I.T., NIT, Ahmedabad, Gujarat, Dec. 2008.
15. Shruti Oza, N.M. Devashrayee, "Low Power Folding Amplifier using 130nm Technology ", *National Conference NUCONE – 2008*, I.T., NIT, Ahmedabad, Gujarat, Dec. 2008.
16. Shruti Oza, N.M. Devashrayee, "MOS Current mode Logic for High Speed Communication using 130nm technology", *National Conference NUCONE – 2009* at I.T., NIT, Ahmedabad, Gujarat, Dec. 2009.
17. Khyati Vachhani, Shruti Oza, " Low Power-High Performance 1-bit Full Adders", *Fourth National Conference on Digital Convergence 2011*, RMK engineering College, Kavaraipettai, Chennai, April 2011.
18. Shruti Oza, N.M. Devashrayee, "Low Power Folding and Interpolating ADC using Cascaded Folding Amplifier." *National Conference NCVDES – 2011*, at CEERI, Pilani, October 12-14, 2011.
19. Jigisha Sureja, Shruti Oza, "An UWB CMOS LNA: Comparative Analysis of various LNA topologies", *National Conference on Power Systems, Embedded Systems, Power Electronics, Communication & Control and Instrumentation (PEPCCI-2012)*, at SVIT, Vasad, Jan. 9-10, 2012.
20. Jigisha Sureja, Shruti Oza, "Design of Cascode CS Wideband LNA using 0.18 μ m CMOS Technology", *National Conference on Advances in Engineering and Technology NCAET – 2012*, at KIT&RC, Kalol, March 9-10, 2012.
21. Urvi Darji, Shruti Oza, Kruti Thakor, "Design of PFD with Charge Pump for DPLL in 0.18 μ m CMOS Technology ", *National Conference on Advances in Engineering and Technology NCAET – 2012*, at KIT&RC, Kalol, March 9-10, 2012.

22. Beena Patel, Shruti Oza, "Low Power Flash ADC Design using 0.18 μ m CMOS Technology ", *National Conference on Advances in Engineering and Technology NCAET – 2012*, at KIT&RC, Kalol, March 9-10, 2012.

Appendix-B

Workshop/STTP attended (32)

1. Short Term Training Programme on Overview of VLSI Design & Technology (ISTE Approved) at IT, Nirma University, Ahmedabad during 18-23rd Dec., 2006.
2. National Workshop on Challenges in VLSI (NWCV-2006) at DA-IICT, Gandhinagar during 21-22nd Dec., 2006.
3. National Workshop on Designing a Modern System-on-Chip for Test (Sponsored by VLSI Society of India) at DA-IICT, Gandhinagar on March 9, 2007.
4. National Seminar on Recent Trends in RF & Wireless Communication (ISTE Approved) at CIT, Changa during 27-28th Feb., 2007.
5. AICTE sponsored Staff Development Programme on Inculcating Research Aptitude in Engineering Faculty at I.T., Nirma University, Ahmedabad during 25th June to 7th July, 2007.
6. ISTE sponsored workshop based on Embedded Technology & Applications at LCIT-Bhandu during 14-16 Feb, 2008.
7. IEEE sponsored workshop based on High Speed Semiconductor Devices at DA-IICT, Gandhinagar-March 9, 2008.
8. IEEE sponsored workshop based on High Speed Semiconductor Devices at DA- IICT, Gandhinagar-March 9, 2008.
9. Second National Seminar on Nanotechnology-Today & Tomorrow on Jan 19, 2009 at IT, Nirma University, Ahmedabad.
10. National Workshop on PhD Research in VLSI at DA-IICT, Gandhinagar on Jan 11, 2009.
11. National Level Workshop on Low Power VLSI Design during Jan. 29-30, 2010 at IT, Nirma University, Ahmedabad.

12. Third National Seminar on Nanotechnology-Today & Tomorrow on March 8, 2010 at IT, Nirma University, Ahmedabad.
13. 6th INUP Familiarization Workshop on Nanofabrication Technologies during Nov. 30-Dec. 4, 2011 at IITB, Mumbai.
14. Patent Search Methodology on Sept. 29, 2012 at LCIT, Bhandu.
15. TEQIP-II sponsored two days workshop on Fundamentals of Cloud Computing during Sept 23-25, 2013 at BVUCOE, Pune.
16. TEQIP-II sponsored three days workshop on Preparing for NBA Accreditaion during Aug 1-2, 2013 at BVUCOE, Pune.
17. Faculty Development Programme on Quality Improvement Schemes of Various Funding Agencies on Octo 19, 2013 at BVUCOE, Pune.
18. One day Seminar on Altium Designer Tool on Feb 7, 2014 at Pune.
19. TEQIP-II sponsored one day workshop on Understanding the mind and conditioning through EFT on July 19, 2014 at BVUCOE, Pune.
20. ISTE approved one week STTP on Challenges in ASIC Design on June 15 to 19, 2015 at VIIT, Pune.
21. Six days workshop on IC Nanometer Design using Mentor Graphics Tool on Aug 31st to Sept 5, 2015.
22. TEQIP-II sponsored two days Workshop on High Impact Presentation Skills by Dale Carnegie Training Centre on Sept 29, 30, 2015 at BVUCOE, Pune.
23. Five days Workshop on HFSS tool on Octo 5-9 2015 at BVUCOE, Pune.
24. Elsevier Connect seminar at Hotel Pride, Pune on October 27, 2015.
25. Five days Workshop on OptiSPICE and OptiSYSTEM tool on Nov 16-20 2015 at BVUCOE, Pune
26. Workshop on RF Design and Simulation tools on Jan 11-16,16 at BVUCOE, Pune-43
27. Workshop on GENSYS on April 4-9, 2016 at BVUCOE, Pune-43
28. Two weeks summer school on Analog CMOS IC Design on June 6-18, 2016 at IIT, Gandhinagar.
29. One day workshop on COMSOL tool on Aug 31, 2016 at Hotel Pride, Pune
30. One-day workshop on COMSOL tool on Aug 31, 2016 at Hotel Pride, Pune
31. Author Workshop by Springer on March 7, 2017 at BVUCOE, Pune-43
32. Elsevier Connect seminar at Hotel Pride, Pune on Nov 8, 2017

Appendix C

Expert Lectures and Session Chair (16):

1. Expert talk on Low Power Design Issues: Deep Sub-micron Region and CMOS Analog Circuits during Work Shop on Advance Trend in low power Consumption 21 -23 July 2011 at LDRP, Gandhinagar
2. Expert talk on Mixed Signal Design during ISTE Approved 2-Week Short Term Training Program on Advances in Electronics & Communication engineering , July 2- 14, 2012 at Institute of Technology, Nirma University, Ahmedabad.
3. Expert lecture on Low Power ADC and Comparator Design for ME, Sem-II (EC-VLSI) students on Feb. 13, 2013 at Institute of Technology, Nirma University, Ahmedabad.
4. Expert lecture with hands on practice for Analog and Mixed Signal Circuit using SPICE Simulator on March 15, 2013 at CIT, Charusat University, Changa.
5. Expert and Session Chair at Annual International Conference-IRAJ on Jan. 19, 2014 at MCCIA, Pune.
6. Expert and Session Chair at Annual International Conference-IRAJ on Feb 23, 2014 at Hotel Madhav International, Pune.
7. Expert and Session Chair at International Conference-ICGTI-2014 at Rizvi College of Engg, Mumbai on March 30, 2014
8. Expert and Session Chair at Annual International Conference-IRAJ on April 27, 2014 at Hotel Sargam, Pune
9. Expert and Session Chair at Annual International Conference-IRAJ on June 29, 2014 at Hotel Madhav International, Pune.
10. Expert talk on Trends in VLSI Design at Institute of Technology, Nirma University, September 27, 2014.
11. Session Chair at Nanocon-2014-BVUCOE on Nov 14-15, 2014.
12. Expert and Session Chair at Annual International Conference-IRAJ on Feb. 01, 2015 at Hotel Madhav International, Pune
13. Session Chair at 1st International conference ETECE-2015 on April 2, 2015 at BVUCOE, Pune
14. Expert and Session Chair at Annual International Conference-IRAJ on April 3, 2015 at BVUCOE, Pune

15. Expert and Session Chair at Annual International Conference-IRAJ on Aug 16, 2015 at Hotel Madhav International, Pune

16. Expert and Session Chair at Annual International Conference-IRAJ on July 3, 2016 at Hotel Madhav International, Pune

Conferences/Workshop/Events Organized (9):

1. State Level Workshop on Mobile Communication during Jan 6-7, 2009 at Kalol Institute of Technology and Research Centre, Kalol.
2. National Conference on Advances in Engineering and Technology (NCAET-2012) during March 9-10, 2012 [Received grant from GUJCOST]
3. Zonal Level Workshop cum Championship-NADC 2013 (Android Application Development in association with ACM and IIT,D) during March 15-16, 2013.
4. National level conference on Advance Technologies for Secured Communication Using 4G & LTE (ATSC-2014) on Feb. 5-6, 2014.
5. 1st International Conference on Emerging Trends in Electronics and Communication Engineering (ETECE-2015) on April 2-3, 2015.
6. National Level Poster Presentation-2016 on September 19, 2016.
7. Faculty Development Programme on Embedded System Design on Octo 20-22, 2016
8. Faculty Development Programme on Signal Processing and Advanced Communication on Feb 27-March 3, 2017
9. Faculty Development Programme on Antenna and Microwave Design on March 20-24, 2017

Appendix-C

Conferences/Workshop Organized (5):

10. State Level Workshop on Mobile Communication during Jan 6-7, 2009 at Kalol Institute of Technology and Research Centre, Kalol.
11. National Conference on Advances in Engineering and Technology (NCAET-2012) during March 9-10, 2012 [Received grant from GUJCOST]
12. Zonal Level Workshop cum Championship-NADC 2013 (Android Application Development in association with ACM and IIT,D) during March 15-16, 2013.
13. National level conference on Advance Technologies for Secured Communication Using 4G & LTE (ATSC-2014) on Feb. 5-6, 2014.
14. 1st International Conference on Emerging Trends in Electronics and Communication Engineering (ETECE-2015) on April 2-3, 2015.