

	<b>Name</b>	<b>Dr. Meena Chavan</b>		
	<b>Designation</b>	<b>Assistant Professor</b>		
	<b>Department</b>	<b>Electronics Engineering</b>		
	<b>Qualification</b>	<b>M.E,Ph.D</b>		
	<b>Contact No.</b>	<b>020-24107390</b>	<b>Ph. Extension</b>	<b>272</b>
	<b>Email ID</b>	<b>mschavan@bvucoep.edu.in</b>		
	<b>Experience</b>	<b>Teaching :</b>	15 Years	<b>Industry :</b> 2.2Years

<b>Area Of Interest</b>	VLSI, Speech processing		
<b>Publications</b>	<b>International Journal (s):</b>	31	<b>National Journals (s) :</b> nil
	<b>International Conference (s):</b>	02	<b>National Conference:</b> 06
<b>Publication Details</b>	<p><b>International Journal:</b></p> <p><b>2019-20</b></p> <ol style="list-style-type: none"> <li>Meena Chavan, "IoT based water quality monitoring using STM 32", IJRAR June 2019, Volume 6, Issue 2.</li> <li>Meena Chavan, "Biometric Based Electronic Voting Machine (Evm) Using Arduino IDE", IRJET, Vol6,Issue6, June2019</li> <li>Meena Chavan, "Haptic Navigational System", IJIRT, Vol6,Issue1 June2019</li> <li>Meena Chavan, " Automated railway crossing",IJIRT,Vol6,Issue1 June2019</li> </ol> <p><b>2018-19</b></p> <ol style="list-style-type: none"> <li>Meena Chavan, "Water Quality Monitoring Using FPGA", IJRDT, Vol-11, Issue-5, May-2019.</li> <li>Meena Chavan, "Reconfigurable low power smart water quality monitoring using FPGA", IJRECE, Vol. 6 Issue 4, Oct- Dec 2018.</li> <li>Meena Chavan, "Survey on Reconfigurable low power smart water quality monitoring using FPGA", IJRECE, Vol. 6 Issue 4, Oct- Dec 2018.</li> <li>Meena Chavan, "Wireless patient monitoring and accidental crisis management system",JETIR, June2018,Vol5,issu6.</li> <li>Meena Chavan, "Reconfigurable low power smart water quality monitoring using FPGA", IJIRSET, June2018, vol7,issu6.</li> </ol> <p><b>2017-18</b></p> <ol style="list-style-type: none"> <li>Meena Chavan, "SOBEL edge detection using HDL codes", IJET,Mar-April2018,vol4,issue4.</li> <li>Ms. Meena Patil, "Separation of Hindi source signal in BSS using GDA-AGSO approach", IJMTER, March 2018 Vol5, issue3.</li> <li>Ms.Meena Patil, "Modified GSO Based Kurtosis Maximization Criterion for</li> </ol>		

BSS in Hindi Speech Processing System Help of LDA”, International Journal of Engineering and Techniques, Jan - Feb 2018, Volume 4 Issue 1

13. Prof. Meena Chavan, “Hardware Co-simulation of Sobel edge detection using FPGA and System generator”, IJIRCCE, Nov. 2017, Vol5, issue 11.
14. Prof. Meena Chavan “ An efficient data hiding approach on digital color image for secret communication, IJETT, Oct. 2017 Vol.52, Issue3.
15. Mrs Meena Chavan, “VLSI based automated digital person counter and smart appliance control system for energy conservation”, IJIRSET, Aug 2017, Vol6, issue-8.

**2016-17**

16. Prof. Meena Chavan “ A review paper on digital image steganography techniques”, IJRASET, May 2017 issue V, vol 5.
17. Mrs. Meena Chavan, “Alcohol Detection and vehicle control system”, IRJET, May 2017, Vol-4, issue-5
18. Mrs Meena Chavan, “VLSI based automated digital person counter and smart appliance control system for energy conservation”, IJIRCCE, May 2017 Vol5, issue-5.
19. Prof. Meena Chavan, “Hardware Co-simulation of Sobel edge detection using FPGA and System generator”, IJRASET, May 2017, Vol5, issue V
20. Mrs. M.S.Chavan, “Implementation of 16-point Radix-4 FFT algorithm”, IJAREEIE, April 2017 vol6, issue4.

**2015-16**

21. Mrs. Meena Chavan, “Detection Of Moving Object Using Background Subtraction CT-DWT Domain,” IJECCE, May 2016 Vol7 Issu3.
22. Mrs. Meena Chavan, “Compressing Encrypted Images & Data Using Auxiliary Information”, IJECCE, May 2016, Vol7 Issu3.
23. Mrs. Meena Chavan, “Over speed alarm system for two wheelers”, IJCR, May 2016, Vol 8, Issu 5.
24. Mrs. Meena Chavan, “Smart real time pollution monitoring & controlling System using FPGA Spartan 3 and Android”, IJSETR, March 2016 Issu 6 Vol 5.
25. Prof. Meena Chavan, “Smart real time pollution monitoring System using FPGA and Android”, IJSART, Dec. 2015. Vol1, issue12.
26. Meena Chavan, “ A wireless approach towards medical surveillance

	<p>system", IJARCCCE, Dec 2015 , vol4,issu12.</p> <p><b>2014-15</b></p> <p>27. Mrs. M. S. Chavan ,” FPGA based multifunctional wireless medical surveillance system,” IJARCCCE-May2015, ISSN No. 2319-5940</p> <p>28. Mrs. M. S. Chavan ,”Source separation and Echo cancellation using Independent component analysis and DWT” IJAREEIE- May2015, Vol4,isu5 ISSN No. 2320-3765</p> <p>29. Mrs. M.S. Chavan,” Blind source separation &amp; echo cancellation using discrete wavelet transform and ICA,” IJSRM-2015, Vol3, issu6 ISSN No. 2321-3418.</p> <p><b>2013-14</b></p> <p>30. Mrs. M. S. Chavan ,” Development of vibration measurement and tilt control ,” IJEEDC-2014,ISSN No.- 2320-2084</p> <p>31. Mrs. M. S. Chavan , “ Finger Print Detection for Medical Application,” IJARCCSEE- 2013 ,ISSN No.- 2277-9043</p> <p><b>International Conference:</b></p> <p>1. Mrs. M. S. Chavan ,” VLSI based speech synthesis”, ICEVD2008</p> <p>2. Mrs. M. S. Chavan,” GSM based remote monitoring system”, ICEVD2008</p> <p><b>National Conference:</b></p> <p>1. Mrs. M. S. Chavan ,” Eye Ball Tracking Wheel Chair ,” NACICE -2013,</p> <p>2. Mrs. M. S. Chavan ,” MIMO Technology to advance Wireless Transmission Networks” NACICE -2013,</p> <p>3. Mrs. M. S. Chavan ,” Wireless Automotive Communications”, NACICE - 2013</p> <p>4. Mrs. M .S. Chavan, “ Automatic Pantry System Using Zigbee Moudle”, NACICE -2013</p> <p>5. Mrs. M.S. Chavan,”Embedded based safe Hybrid system for cars”, NCECIS-2011</p> <p>6. Mrs. M.S. Chavan,”Solaries 10 os zonar technology for visulization concept” NCECIS-2011</p> <p><b>National level Symposium:</b></p> <p>1. Mrs. M.S. Chavan, “4G Technology”.</p>
<b>Books Published</b>	nil
<b>Professional Memberships</b>	ISTE
<b>WorkShop/Seminar/Conference attended</b>	1. Attended one week STTP “Assessment and Evaluation under Outcome Based Education”, NITTTR from 10 <sup>th</sup> to 14 <sup>th</sup> June2019.

	<ol style="list-style-type: none"> <li>2. Successfully completed NPTEL- AICTE faculty development program on “Cloud Computing”, Feb-April 2019.</li> <li>3. Attended faculty development program on, “ Electronic Design Automation with Mentor Graphics back end tool”, At BVCOEW, Pune from 21<sup>st</sup> to 23<sup>rd</sup> Feb 2019.</li> <li>4. Attended National Teacher’s Congress-2 at MIT WPU, Pune, from 10-12<sup>th</sup> Jan 2018</li> <li>5. Attended Workshop , ‘Springer Author’, at BVUCOE, Pune Mar 2017</li> <li>6. Successfully completed Short term training program on “Recent trends in advanced communication and Embedded Systems from BV’s COE, Kolhapur from 24-29<sup>th</sup> Dec. 2016.</li> <li>7. Attended one week Faculty Development Program On” Advanced Embedded Technology” At SIT COE, Yadrav, Ichalkaranji, Dec. 2016</li> <li>8. Attended Workshop on “photonic Design and simulation tools” at BVUCOE, Pune, Nov. 2015.</li> <li>9. Attended one week Workshop on “IC nanometer design using Mentor Graphics tool” at BVUCOE, Pune 31 Aug-Sep 2015</li> <li>10. Workshop” Secrets of Guru”, BVDUCOE, Pune, Aug 2015.</li> <li>11. Workshop “Women Empowerment,” at BVDUCOE, Pune, Feb. 2015.</li> <li>12. Workshop “Embedded system design using ARM cores”, PICT, Pune Dec. 2014</li> <li>13. Workshop “ Fundamentals of Cloud Computing”, BVDUCOE, Pune Aug 2013</li> <li>14. Workshop “LABVIEW” ,BVDUCOE, Pune Jan. 2013</li> <li>15. Certificate course in “CMOS VLSI Design”, PICT, Pune, Sep. 2012</li> <li>16. Seminar “MATLAB &amp; Simulink”, Mathworks, Pune 2011</li> <li>17. Workshop “ROBOTICS”, MIT, Pune 2011</li> <li>18. STTP “Advanced VLSI &amp; Microelectronics Design”, MIT, Pune Mar 2010</li> <li>19. Workshop “Hands on PIC Microcontroller” SKN, Pune, Feb 2010</li> <li>20. Workshop “3G Technology”, PVPIT, Pune 2009</li> <li>21. Attended and completed five days short term training program (STTP) on “Advanced VLSI” from Bharati Vidyapeeth University College of Engineering, Pune. 2008</li> <li>22. Completed a three days workshop on FACE (Faculty Awareness Camp on Entrepreneurship) from Bharati Vidyapeeth University College of Engineering, Pune. 2008.</li> <li>23. Workshop “Teaching Engineering Effectively”, BVDUCOE, Pune 2006</li> </ol>
<b>Achievements</b>	02 patents filed, First prize for project at IISF 2018, Lucknow.
<b>Extra Activities</b>	<ol style="list-style-type: none"> <li>1. Admission committee member.</li> <li>2. Bharatiyam committee member.</li> <li>3. Workshop coordinator (ESDAP-15, ESD-16, ESD&amp;IoT-17, SMSC-2018, PCB design &amp; Fabrication).</li> <li>4. Unit Test Coordinator.</li> <li>5. Participated in Swachh Bharat Abhiyan Organized By Department of Electronics BVUCOE at Vitthalwadi, Pune.</li> <li>6. Participated in Swachh Bharat Abhiyan Organized By BVUCOE at Pune Station.</li> <li>7. Participated in ‘Beti Bachao, Beti Padhao’ Abhiyan at Khedshivapur.</li> <li>8. Board of Studies Member.</li> <li>9. NBA coordinator.</li> </ol>

	10. Library Committee member.
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