

**BHARATI VIDYAPEETH (DEEMED TO BE UNIVERSITY) COLLEGE OF ENGINEERING  
DHANKAWADI, PUNE – 411043**

**Class Time Table (TERM - II A.Y. 2019-20)**

Name of the Department : : Electronics & Telecommunication Engineering Department

Class: B. Tech. ( E & TC 1 ) Sem. –VI

Class Room No.: W 401

Time	Monday	Tuesday	Wednesday	Thursday	Friday	Time	Saturday
09:15	DSP-PVD-E1 W407A ES-SCM-E2 W512 VD-SSB-	DSP-SAK-E2 W407A ES-SCM-E3 W512 VD-SSB-E4 W407B	DSP-SAK-E3 W407A ES-NKS- E4 W512 VD- SSB-E1 W407B	DSP-SAK-E4 W407A ES-SCM-E1 W512 MTA-VF-E2 W510	Electronic Circuit Design Series by RBG W406B	08:30	
10:15	E3 W407B MTA- SST-E4 W510	MTA-VF-E1 W510	MTA-SST-E3 W510	VD-SSB-E2 W407B		09:30	DSP-VHP E&TC I SEM VI W401
11:15	RECESS					10:30	
11:30	DSP-SAK E&TC I SEM VI W401	VD-SKO E&TC I SEM VI W401	MTA-SST E&TC I SEM VI W401	MTA-SST E&TC I SEM VI W401	VD-SKO E&TC I SEM VI W401	11:30	PSD-E&TC I SEM VI W406B
12:30	ITC-CSM E&TC I SEM VI W401	ITC-CSM E&TC I SEM VI W401	ITC-CSM E&TC I SEM VI W401	DSP-SAK E&TC I SEM VI W401	DSP-SAK E&TC I SEM VI W401	12:30	
01:30	RECESS						
02:15	VD-SSB E&TC I SEM VI W401	ES-SSB E&TC I SEM VI W401	ES-SSB E&TC I SEM VI W401	PSD-E&TC I SEM VI W406B	Electronic Circuit Design Series by RBG W401		
03:15	MTA-AYP E&TC I SEM VI W401	ES-SSB E&TC I SEM VI W401					
	<b>Faculty Name</b>	<b>Subject</b>		<b>L</b>	<b>T</b>	<b>P</b>	<b>Total</b>
	SAK Prof. S A Kadam/VHP Prof. V H Patil	Digital Signal Processing		4	0	8	12
	SST / AYP Prabhakar	Microwave Theory and Antenna		3	0	8	11
	CSM Prof. C S More	Information Theory and Coding		3			
	SKO- Dr. Shruti Oza/ SSB Prof. S S Bussa	VLSI Design		3	0	8	11
	SSB Prof. S S Bussa	Embedded Systems		3	0	8	11
	VF Visiting Faculty	Professional Skills Development		4	0	0	4
		<b>Total</b>		20	0	32	49