

**BHARATI VIDYAPEETH (DEEMED TO BE UNIVERSITY)**  
**COLLEGE OF ENGINEERING**  
**DHANKAWADI, PUNE – 411043**  
**Electronics Engineering Department**

**Class Time Table (TERM -II A.Y. 2019-20)**

CLASS: M.Tech. (Electronics-VLSI) SEM:II CLASS ROOM NO: W-416

(w.e.f. 01/ 01/ 2020)

PG Coordinator: Vikas Kaduskar (Mobile -8983368947)

TIME	MON	TUE	WED	THU	FRI	TIME	SAT
09:15		ADSP MVP		I & V P SG	AVLSID SKO	8.30	
10:15		AVLSID SKO		I & V P SG	WN VPK	9.30	
11:15	R E C E S S					10:30	STAFF MEETING
11:30	ADSP MVP	WN VPK	AVLSID SKO	ADSP AAS	ADSP AAS	11:30	
12:30	AVLSID SKO	I & V P SG	I & V P SG	WN SPG	WN SPG	12:30	
1:30	R E C E S S						
02:15	ADSP W305-SRM		AVLSID				
03:15			W305- SG				

Faculty Name	Subject	L	T	P	Total
A.A. SHINDE	Advanced DSP	02	-	-	02
M.V. PATIL	Advanced DSP	02	-	-	02
S.K.OZA	Analog VLSI Design	04	-	-	04
S. GHUGARDARE	Image & Video Processing	04	-	-	04
V.P.KADUSKAR	Wireless Network	02	-	-	02
S.P. GAIKWAD	Wireless Network	02	-	-	02
S.R. MADKAR	Advanced DSP	-	-	02	02
S. GHUGARDARE	Analog VLSI Design	-	-	02	02
TOTAL		16	00	04	20