

**BHARATI VIDYAPEETH (DEEMED TO BE UNIVERSITY)**  
**COLLEGE OF ENGINEERING, PUNE-43**  
**Department of Computer Engineering**

**Class Time Table (TERM- I A.Y.2019-2020)**

**B.TECH COMPUTER SEM –III DIV-I**  
**GFM – Prof. Mayuri Molawade (9011169977)**

**Class Room: - WW-408**  
**w.e.f :03/07/2019**

Time	Monday	Tuesday	Wednesday	Thursday	Friday	Time	Saturday
9:15 to 10:15	FDC	FDC	DTLD	PSD	PSD	8:30 to 9:30	PSD
10:15 to 11:15	DMGT	EEM	FDC	FDC	PSD	9:30 to 10:30	EEM
11.15 to 11.30	<b>RECESS</b>					10:30 to 11.30	<b>RECESS</b>
11:30 to 12:30	PL-I(A) DMGT(B) PL-I(C) ↑ ↓	DMGT(A) PDS(B) DTLD(C) ↑ ↓	PL-I(B) DMGT(C) DTLD(A) ↑ ↓	EEM  DMGT	Network Security ↑ ↓		
12:30 to 1:30							
1:30 to 2:15	<b>RECESS</b>					11:30 to 12:30	PL-I(A) DTLD(B) PDS(C) ↑ ↓
2:15 to 3:15	PDS	DTLD		PL-I(B) PDS(A) PL-I(C) ↑ ↓	DMGT	12 :30 to 1:30	
3:15 to 4:15	DTLD	PDS			PDS		

Sr.No	Subject Name	Faculty Name	L	P	T	LAB
1.	Fundamentals of Data Communication	Mr.Akash Suryavanshi	3	-	1	--
2.	Principles of Data Structures	Mrs.Dipti Pawar	3	6	-	Batch A, B <b>WW-II-410(PL-II Lab)</b>
		Mr.Akash Suryavanshi				Batch C <b>WW-II-402 (CG Lab)</b>
3.	Digital Techniques & Logic Design	Mrs.Gauri Rao	3		-	
		Mrs.Mayuri Molawade		6		Batch:(A,B,C) <b>WW-II-406(MPMC Lab)</b>
4.	Discrete Mathematics & Graph Theory	Dr.A.K.Kadam	3	6	-	Batch:(A,B,C) <b>WW-II-401(SS Lab)</b>
5.	Engineering Economics & Management	Mrs.RohiniKhalkar	3	-	-	--
6.	Professional Skill Development-III	V	4	-	-	--
7.	Programming Lab-I	Mr.Saurabh Saoji	-	8	-	Batch A,B <b>WW-II-411(PL-I Lab)</b>
		Mr.Sagar Mohite	-	4	-	Batch C <b>WW-II-415 (OS Lab)</b>

**Time Table In-charge**

**H.O.D.**