

**BHARATI VIDYAPEETH
(DEEMED TO BE UNIVERSITY)
COLLEGE OF ENGINEERING
DHANKAWADI, PUNE – 411043
Electronics Engineering Department**

Class Time Table (TERM -I A.Y. 2019-20)

CLASS: B.Tech. (Electronics) SEM: III
GFM :- Mrs. J.S. Morbale

CLASS ROOM NO: W-301

(w.e.f. 03/07/2019)

TIME	MON	TUE	WED	THU	FRI	TIME	SAT
9:15	PSD-III	AE JSM	C-AE- JSM -W-403; D-CT-SPT-W307B;	EM-III VF	E-AE-JSM-W-403; F-CT-SPT-W307B;	8.30	F-AE-NTM-W-403; A-CT-ASN-W307B;
10:15	PSD-III	DLC PRN	E-DLC-PRN-W310; F-SS-SVD-W302	AE JSM	A-DLC-SPK-W310; B-SS-SVD-W302	9.30	B-DLC-PRN-W310; C-SS-SVD-W302
11:15	R E C E S S					10:30	STAFF MEETING
11:30	A-AE-JSM-W-403; B-CT-ASN-W307B;	B-AE -JSM -W-403; C-CT-SPT-W307B;	AE JSM	D-AE- JSM -W-403; E-CT-SPT-W307B;	EM-III VF	11:30	PSD-III
12:30	C-DLC-PRN-W310; D-SS-MVP-W302	D-DLC-SPG-W310; E-SS-MVP-W302	DLC PRN	F-DLC-PRN-W310; A-SS-MVP-W302	AE JSM	12.30	PSD-III
1:30	R E C E S S						
2:15	CT SPT	SS MVP	SS MVP	EM-III (Tut) VF	CT SPT		
3:15	SS MVP	EM-III VF	CT SPT		DLC PRN		

Faculty Name	Subject	L	T	P	Total
J.S.MORBALE	AE - ANALOG ELECTRONICS	04	-	10	14
N.T. MARKAD	AE- ANALOG ELECTRONICS			02	02
S. P. TONDARE	CT- CIRCUIT THEORY	03	-	08	11
A.S. NIGADE	CT- CIRCUIT THEORY	-	-	04	04
P.R. NAREGALKAR	DLC -DIGITAL LOGIC CIRCUITS	03	-	08	04
S.P. GAIKWAD	DLC- DIGITAL LOGIC CIRCUITS			02	02
S.M. KHATAVKAR	DLC- DIGITAL LOGIC CIRCUITS			02	02
M. V. PATIL	SS- SIGNALS & SYSTEMS	03	-	08	11
S.V.DHOLE	SS- SIGNALS & SYSTEMS	-	-	06	06
VISITING FACULTY	EM-III ENGG. MATHS-III	03	01	-	04
VISITING FACULTY	PSD-III PROFESSIONAL SKILL DEVELOPMENT III	-	-	04	04
TOTAL		16	01	50	67