

**BHARATI VIDYAPEETH**  
**(DEEMED TO BE UNIVERSITY)**  
**DHANKAWADI, PUNE – 411043**  
**Electronics Engineering Department**  
**Class Time Table (TERM -I A.Y. 2019-20)**

**CLASS: M. Tech. (Electronics-VLSI) SEM: I      CLASS ROOM NO: W-416**  
**PG Coordinator: Vikas Kaduskar    (Mobile -8983368947)      (w.e.f. 15/07/2019)**

TIME	MON	TUE	WED	THU	FRI	TIME	SAT
9:15	ACS NTM				ESP MSC	8.30	
10:15	ESP AAS				ACS NTM	9.30	
11.15	R   E   C   E   S   S					10.30	<b>Staff meeting</b>
11.30	DVLSI RG	ACS NTM	ESP MVP W 304	DVLSI RG		11:30	LA & RP VF
12.30	LA & RP VF	ACS NTM		LA & RP VF		12:30	LA & RP VF
1.30	R   E   C   E   S   S						
2.15		DVLSI RG	DVLSI RG	DVLSI ARP W-305			
3.15		ESP AAS	ESP MSC				

Faculty Name	Subject	L	T	P	Total
A.A.SHINDE	Embedded System& Processors	02	-	-	02
M.S.CHAVAN	Embedded System& Processors				
M.V. PATIL	Embedded System& Processors	-	-	02	02
R. GHONGADE	Digital VLSI	04	-	00	04
A.R.PATIL	Digital VLSI	-	-	02	02
N.T.MARKAD	ACS- Advanced Communication System;	04	-	-	04
VISITING FACULTY	LA & RP- Linear Algebra & Random processes;	04	-	-	04
Total		16	00	04	20