

**BHARATI VIDYAPEETH
(DEEMED TO BE UNIVERSITY)
DHANKAWADI, PUNE – 411043
Electronics Engineering Department
Class Time Table (TERM –I A.Y. 2019-20)**

CLASS: M. Tech. (Electronics-VLSI) SEM: III

CLASS ROOM NO: PG Lab (PSOC W-304)

PG Coordinator: Vikas Kaduskar (Mobile -8983368947)

(w.e.f. 03/07/2019)

TIME	MON	TUE	WED	THU	FRI	TIME	SAT
9:15	ELECTIVE-II (AFVDA) VF W-304	ELECTIVE I TVVD SPT W-304	ELECTIVE-I (TVVD) SKO W-416			8.30	Self Study-I
10:15	ELECTIV II (AFVDA) VF W-304		ELECTIVE I (TVVD) SKO W-416			9.30	
11.15	R E C E S S					10.30	
11.30	ELECTIVEII (AFVDA) VF W-305	ELECTIVE I (TVVD) SKO W-416	ELECTIVE-II (AFVDA) VF			11:30	Dissertation - I
12.30			ELECTIV II (AFVDA) VF			12:30	
1.30	R E C E S S						
2.15	ELECTIVE-I (TVVD) SKO W-416			Self Study-I	Self Study-I		
3.15	Self Study-I						

Faculty Name	Subject	L	T	P	Total
S.K. OZA	TESTING & VERIFICATION OF VLSI DESIGN	04	-	-	04
S. P. TONDARE	TESTING & VERIFICATION OF VLSI DESIGN			02	02
VISITING FACULTY	ALGORITHM FOR VLSI DESIGN IN AUTOMATION	04	-	02	06
	DISSERATION REPORT-I	-	-	04	04
	SELF STUDY -I			04	04