Name	Mrs. Payal Sanjay Kadam
Designation	Assistant professor
Department	Electronics and Telecommunication
Qualification	M.Tech. (Electronics & Tele Communication)
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Experience	Teaching : 4 Years Industry :

Area Of Interest	VLSI and Embedded System

Publications	International Journal (s): 01	National Journals (s) : 00
	International Conference (s): 00	National Conference: 00

Publication Details	Ms. Payal S. Kadam, Prof. S.S.Belsare,"FPGA Implementation of Reconfigurable Processor for Image Processing" International Journal Of Scientific & Engineering Research, Volume 8, Issue 4, April-2017;

Books Published	
Professional Memberships	

WorkShop/Seminar/Conference attended	Webinar on open access and quality(comparing database , inclusivrity , questionable publishers) by DOAJ Worskshop on Intellectual Property Rights Awareness:Patent Drafting and filing Webinar on Resaerch Paoers:Concept of Submission Workshop on Time Series Analysis & Distributed Computing Webinar on Cognitive Radio Webinar on Image Processing using PYTHON Webinar on Virtual Reality TEQIP -2 Workshop on ESD using ARM & PSOC Training Programme in HEM ELECTRONICS based on PLC, SCADA Automation Based on PLC, SCADA ,HMI in MPTA Education.

Achievements	
Extra Activities	Guardian Faculty Member, Member of Academic Committee, Department R&D coordinator