

	Name	Mr. Sudhir Bussa		
	Designation	Assistant professor		
	Department	E&TC engineering		
	Qualification	M.Tech (Electronics- Computer)		
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Experience	Teaching :	10.6 Years	Industry :	-

Area of Interest	VLSI, Embedded Systems, Machine Learning			
Publications	International Journal (s):	08	National Journals (s) :	0
	International Conference (s):	0	National Conference:	01
Publication Details	<ul style="list-style-type: none"> • “Ocular Movement based Assistance for Catatonic Patients” International Journal of Engineering Research & Technology (IJERT) ISSN: 2278-0181 Vol. 9 Issue 03, March-2020 • “Smart Attendance System using OPENCV based on Facial Recognition” International Journal of Engineering Research & Technology (IJERT) ISSN: 2278-0181 Vol. 9 Issue 03, March-2020 • “Malaria Parasite Detection using Various Machine Learning Algorithms and Image Processing” SSRG International Journal of Computer Science and Engineering (SSRG-IJCSE) – Volume 7 Issue 2 – February 2020 ISSN: 2348 – 8387 • "Implementation of Fast Multiplier", International Journal of Industrial Electronics and Electrical Engineering (IJIEEE), Volume-6, Issue-3 (Mar, 2018) • "Smart Help ", International Journal of Industrial Electronics and Electrical Engineering (IJIEEE), Volume-6, Issue-3 (Mar, 2018) • "Voice Assisted Home Automation ", International Journal of Industrial Electronics and Electrical Engineering (IJIEEE), Volume-6, Issue-3 (Mar, 2018) • “Design of Rectangular Micro-Strip Patch antenna for 916 Mhz.”, 			

	<p>International Journal of Engineering Sciences & Research Technology ISSN 2277-9655- May 2017</p> <ul style="list-style-type: none"> • “Design of Binary Multiplier using Adders”, International Journal of Electrical and Electronics Research ISSN 2348-6988 (online) Vol. 4, Issue 1, pp: (169-173), Month: January - March 2016 • “Implementation of Image enhancement on FPGA”, National conference in Electronics & Instrumentation-2013
Books Published	Nil
Professional Memberships	Nil
WorkShop/ Seminar/Conference attended	<p>Online FDP on “Advancements and research opportunities in Electronics and Telecommunication Engineering “ organized by department of E&TC, Bharati Vidyapeeth Deemed to be University, College of Engineering, Pune held in 4th August to 8th August 2020.</p> <p>IIC Online Sessions conducted by IIC MHRD’s Innovation, IPR, Entrepreneurship, and Start-ups from 28th April to 22nd May 2020.</p> <p>MHRD’s innovation cell-India First Leadership Talk Series- webinar on 9th May, 30th May, 6th June and 20th June, 2020.</p> <p>Faculty development Programme on Optical fiber communication , Organized by department of E&TC, Bharati Vidyapeeth Deemed to be University, College of Engineering, Pune from 4th to 8th November, 2019</p> <p>Faculty development Programme on Machine Learning Techniques using MATLAB, Organized by department of E&TC, Bharati Vidyapeeth Deemed to be University, College of Engineering, Pune from 14th May to 18th May, 2018</p> <p>Workshop attended -NI LABVIEW, Good Teaching Practices & challenges, Workshop on Cloud Competing, Embedded System design, Red Hat Linux, PSOC and ARM, Ansys HFSS, Optispice & OptiSystem</p> <p>Conferences:</p> <p>1st International Conference on Emerging Trends in Electronics and Communication Engineering on April 2-3, 2015</p> <p>A National Conference on “Advance Technologies for Secured Communication using 4G and LTE (ATSC-2014)”</p> <p>NI seminar, Agilent Seminar</p>
Achievements	<p>Ranked among top 2%, passed with Elite+Gold certificate in NPTEL course examination of course “ Embedded System Design with ARM” during Jan-Mar 2019</p> <p>Qualified in Gate-2008,2011,2012,2013</p>
Extra Activities	Guest speaker at School of Audiology, Bharati Vidyapeeth , External faculty to VIT ,

	Paper presentation committee member of “Bharatiyam”, ETSA committee Faculty Incharge, Embedded Systems Lab Incharge.
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